

Fig. 1

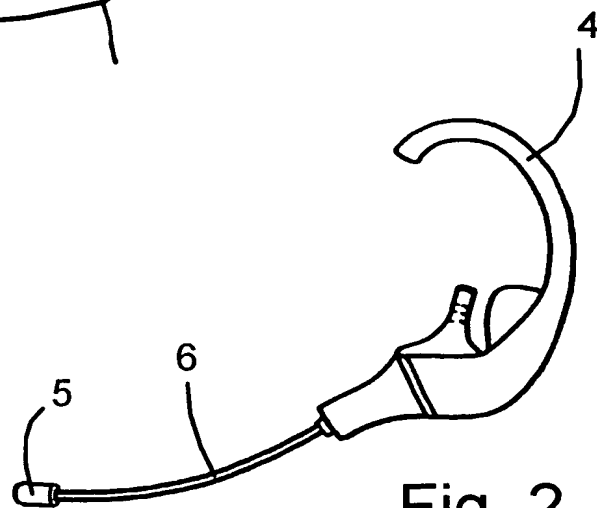


Fig. 2

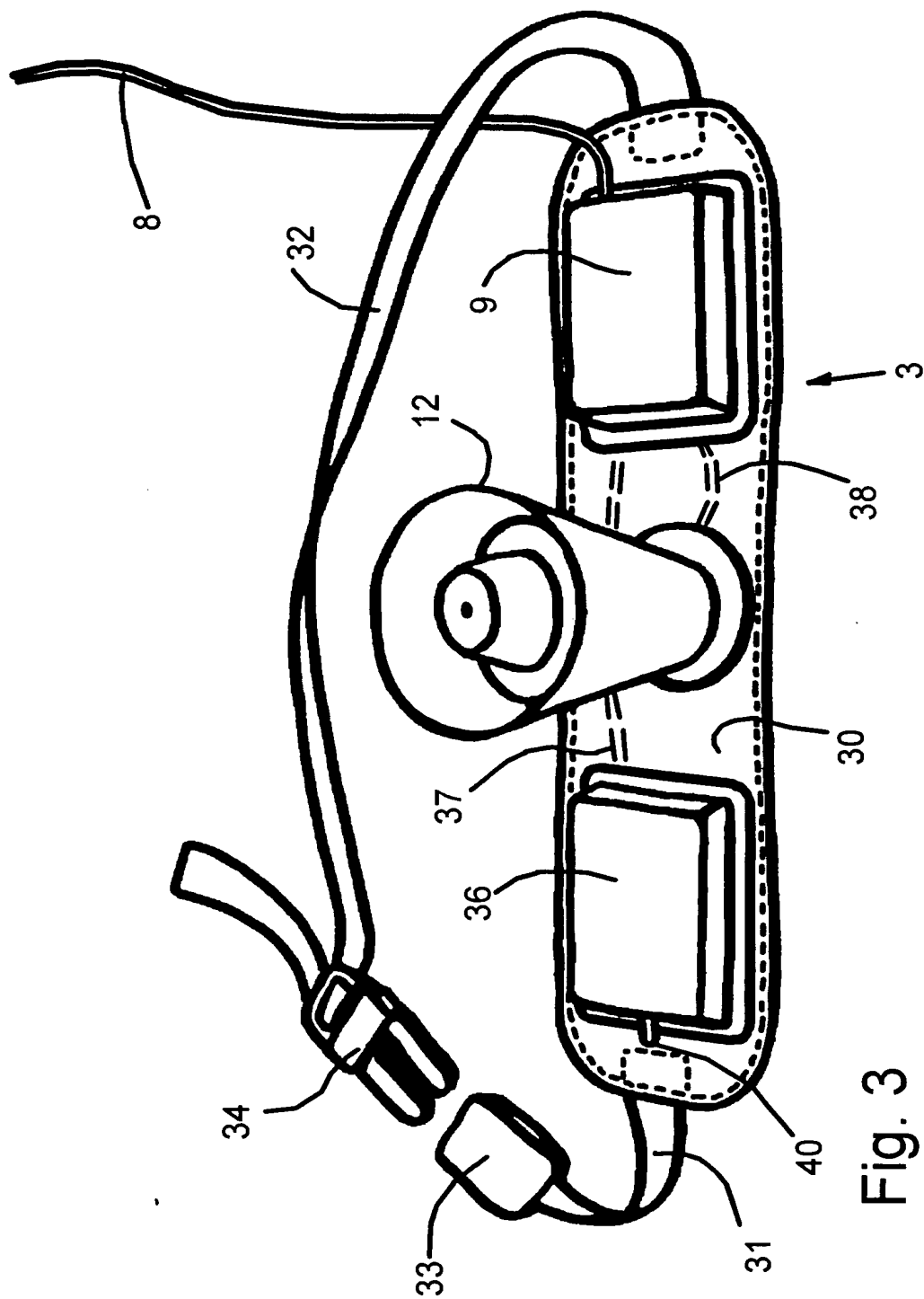


Fig. 3

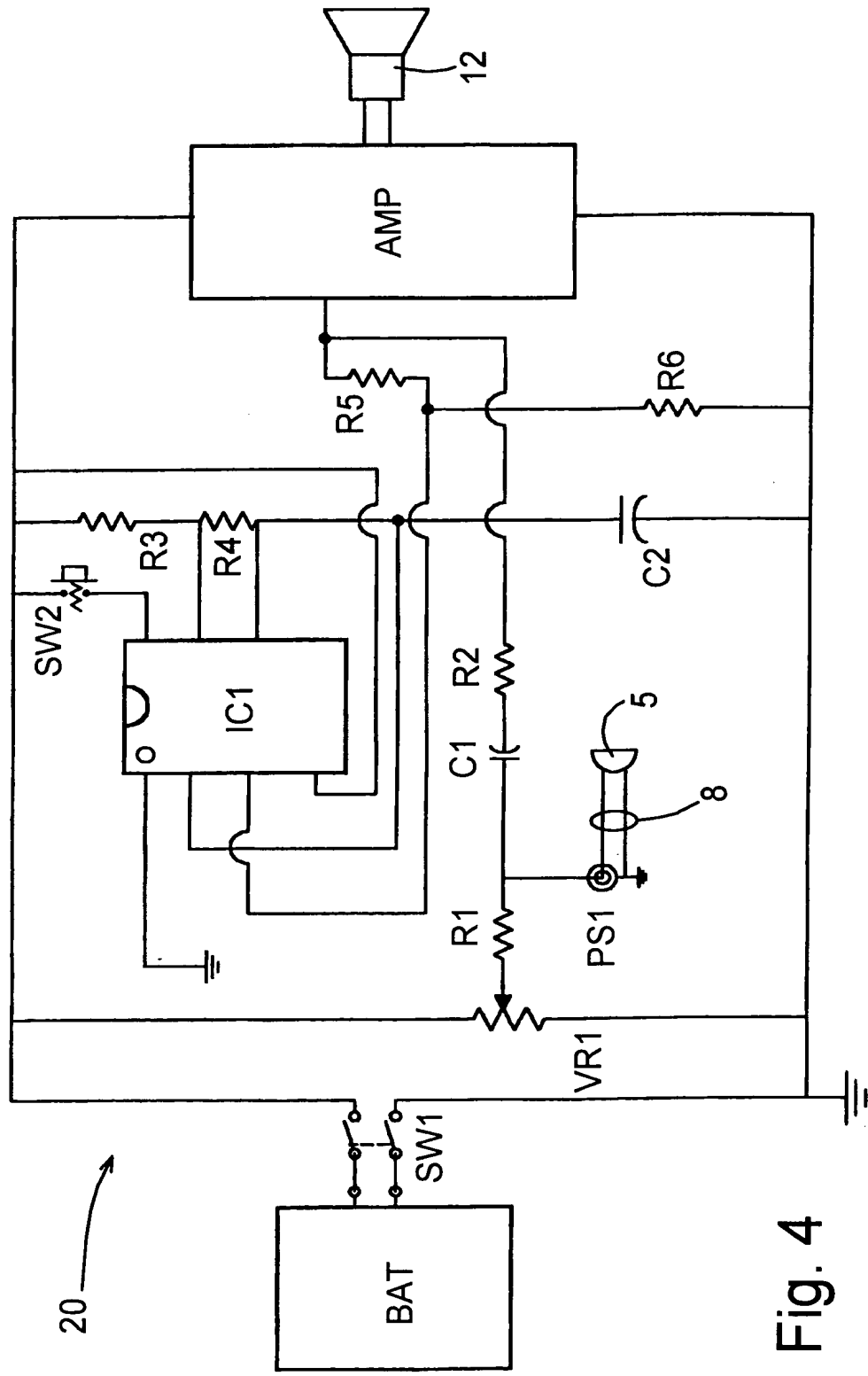


Fig. 4

FIG. 4 is a schematic diagram of the circuit of the present invention. The circuit includes a battery (BAT), a switch (SW1), a variable resistor (VR1), a power source (PS1), a series combination of resistors R1 and R2, a parallel combination of a capacitor C1 and a component 5, a parallel combination of a capacitor C2, a switch (SW2), a network of resistors R3 and R4, an integrated circuit (IC1), a resistor R5, an amplifier (AMP), a resistor R6, and a speaker (12). The circuit is grounded at the bottom right corner.